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10/751,607

01/05/2004

Yi-Jen Wu

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EXAMINER

WILLIAMS, JOSEPH L

ART UNIT

PAPER NUMBER

2879

MAIL DATE

DELIVERY MODE

05/18/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/751,607

Applicant(s)

WU ET AL.

Examiner

Joseph L. Williams

Art Unit

2879

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 14 February 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 21-38 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 21-38 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 14 February 2007 has been entered.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 21, 22, 24-27, 29, 31-38 are rejected under 35 U.S.C. 102(e) as being anticipated by Murai et al. (US 2006/0132039 A1).

Regarding claim 21, Murai ('039) teaches in figures 8, 12, and the corresponding text, an electrode (22, 23) for a plasma display panel, comprising: at least one bus line (221, 231) conductor; and at least one pad (222, 232) connected with the at least one

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bus line conductor, the at least one pad including opposing side sections (8b) that point away from one another.

Regarding claim 22, Murai ('039) teaches the side sections is triangular in shape (figure 8b).

Regarding claim 24, Murai ('039) teaches the side sections of the at least one pad each have a portion that gradually increases in width.

Regarding claim 25, Murai ('039) teaches the side sections of the at least one pad gradually increase in width.

Regarding claim 26, Murai ('039) teaches the at least one bus line conductor has a first width and the side sections have a maximum width that is greater than the first width, each of the side sections having a portion that is narrower than the maximum width and that intersects the at least one bus line conductor.

Regarding claim 27, Murai ('039) teaches an electrode (figure 8e) for a plasma display panel, comprising: at least one bus line conductor, and at least one pad connected with the at least one bus line conductor, the at least one pad including opposing side sections, each of the side sections having a blunted triangular shape, the side sections tapering away from an interior portion of the at least one pad.

Regarding claim 29, Murai ('039) teaches the interior portion of the at least one pad includes blunted triangular shape end portions, one of the end portions connecting with the at least one bus line conductor.

Regarding claim 31, Murai ('039) teaches the side sections to be at least one pad each have a portion that gradually increases in width.

Regarding claim 32, Murai ('039) teaches the side sections of the at least one pad gradually increase in width.

Regarding claim 33, Murai ('039) teaches the at least one bus line conductor has a first width and the side sections have a maximum width that is greater than the first width, each of the side sections having a portion that is narrower than the maximum width and that intersects the at least one bus line conductor.

Regarding claim 34, Murai ('039) teaches an electrode for a plasma display panel, comprising: at least one bus line conductor; and at least one pad connected with the at least one bus line conductor, the at least one pad having an oval shape (figure 8c).

Regarding claim 35, Murai ('039) teaches the at least one pad gradually increases in width.

Regarding claim 36, Murai ('039) teaches the at least one bus line conductor has a first width and side sections of the at least one pad have a maximum width that is greater than the first width, each of the side sections having a portion that is narrower than the maximum width and that intersects the at least one bus line conductor.

Regarding claim 21, Murai ('039) teaches an electrode for a plasma display panel, comprising: at least one bus line conductor; and at least one pad including tapering end sections and a bulging inner section, one of the tapering end sections connected with the at least one bus line conductor (figure 8f).

Regarding claim 21, Murai ('039) teaches the at least one bus line conductor has a first width and the tapering end sections have a maximum width that is greater than

the first width, each of the tapering end sections having a portion that is narrower than the maximum width and that intersects the at least one bus line conductor.

Claims 21, 23, 28, and 30 are rejected under 35 U.S.C. 102(e) as being anticipated by Su (US 2005/0174057).

Regarding claim 21, Su ('039) teaches in figures 8, 9, and the corresponding text, an electrode (X, Y) for a plasma display panel, comprising: at least one bus line (93) conductor; and at least one pad connected with the at least one bus line conductor, the at least one pad including opposing side sections that point away from one another (figure 8e).

Regarding claim 23, Su ('039) teaches the side sections has a triangular portion and a rectangular portion, the rectangular portion having a line width which is less than a line width of the at least one bus line conductor.

Regarding claim 28, Su ('039) teaches the interior portion of the at least one pad is rectangular in shape.

Regarding claim 30, Su ('039) teaches the at least one pad has an octagonal shape.

Conclusion

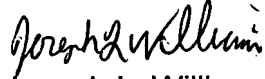
3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Hirose et al. (US 7,166,394 B2) teaches the state of the art for pad electrode structure.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph L. Williams whose telephone number is (571) 272-2465. The examiner can normally be reached on M-F (6:30 AM-3:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimeshkumar D. Patel can be reached on (571) 272-2457. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


Joseph L. Williams
Primary Examiner
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